




# PHILADELPHIA UNIVERSITY LIBRARY

## ONLINE CATALOGUE

### Advanced digital logic design using VHDL, state machines and synthesis for FPGAs

	<b>Author</b>	Lee, Sunggu (Author)
	<b>Responsibility Data</b>	Sunggu Lee
	<b>Publication Data</b>	Toronto, Ontario: Thomson, 2006
	<b>Physical Description</b>	xii, 488 p.
	<b>ISBN</b>	0-534-46602-8
	<b>Subject Area</b>	Engineering
	<b>Subject Headings</b>	Logic design -- Data processing Digital electronics VHDL (Computer hardware description language) Sequential machine theory Application specific integrated circuits
	<b>Call Number</b>	621.395 LEE