Philadelphia University

Faculty of Engineering

Question 1 Multiple Choices



Student Name: Student Number: Serial Number:

(5 marks)

First Exam, Second Semester: 2018/2019 Dept. of Computer Engineering

Course Title:MicroprocessorsDate:02/04/2019Course No:0630313Time Allowed:50 minutesLecturer:Dr. Qadri HamarshehNo. Of Pages:3

Instructions:

- ALLOWED: pens and drawing tools (no red color).
- NOT ALLOWED: Papers, calculators, literatures. Otherwise, it will lead to the non-approval of your examination.
- Shut down Telephones, and other communication devices.

Please note:

- This exam paper contains 5 questions totaling 20 marks
- Write your name and your matriculation number on every page of the solution sheets.
- All solutions together with solution methods (explanatory statement) must be inserted in the labelled position on the solution sheets.

Basic notions: The aim of the questions in this part is to evaluate the required minimal student knowledge and skills. Answers in the pass category represent the minimum understanding of basic concepts of IA-32 Processor Architecture, Assembly Language Fundamentals: Instructions, Directives, Identifiers, Defining Data, Symbolic Constants, Data Transfers, Addressing, and Arithmetic instructions.

Identify the choice that best completes the statement or answers the question.	
1) During which phase of the instruction execution cycle is the program counter (IP regist	er)

	incremented?				
	a)	decode	b)	execute	
	c)	operand fetch	d)	fetch	
2)	The size	e of each segment in 8086 is	S		
-	a)	16kb	b)	24 kb	
	c)	50 kb	d)	64 kb	
3)	The Ov	rerflow flag is based on sig	gned arithmet	ic.	
	a)	True	b)	False	
4)	Which o	of these values is the most ac	cceptable for	the data type R l	EAL4 ?

5) One of the following **memory models** combines the data and code parts:

b)

d)

"Jennifer",0

a) Small b) Tiny
c) Meduim d) Huge

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23.26

a)

C)

Familiar and Unfamiliar problems solving: the aim of the questions in this part is to evaluate that the student has some basic knowledge of the key aspects of the lecture material and can attempt to solve familiar and unfamiliar problems of IA-32 Processor Architecture, Assembly Language Fundamentals: Instructions, Directives, Identifiers, Defining Data, Symbolic Constants, Data Transfers, Addressing, and Arithmetic instructions.

Question 2	(3 marks)
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Explain the Real-Address Mode Programming (16-bit MS-DOS Programs): Advantages, Disadvantages and Requirements needed in your assembly code.

Solution	
Question 3	(5 marks)

Use the following data definitions:

byte1 BYTE 0FFh,1,2 byte2 BYTE 14h

word1 WORD 0FFFFh,1,2

word2 WORD 3

word3 SWORD 7FFFh, 8000h

a) For each of the following instructions, indicate whether it is **legal (L)** or **illegal (I)**:

Ñ		Instruction	Answer
1	l)	mov word1, byte2	
2	2)	mov word2,10000h	
3	3)	mov si, ds	
4	l)	movzx ax, bytel	
5	5)	movsx dl,al	

b) Indicate the hexadecimal value of the destination operand next to each instruction. Use the letter **I** to indicate that a particular instruction is **illegal**:

mov ax,wora3	DX =
movsx eax,bytel	EAX =
mov dh,al	DH =
mov ax,[word3+2]	AX =
mov eax,[word3+4]	EAX =

Question 4 (3 marks)

MASM can't assemble this program because of **some errors**. Rewrite the program to be assembled without errors.

Original Program	Your answer
INCLUDE irvine32.inc	
.data arrayA word 4Ah, 3Ch, 2h, 5 DUP (1h) varl word ffffh	
.code main PROC mov cx,4Ch xchg cx, arrayA+4 mov ax, arrayA+5 mov bl, varl add ax, bx call dumpregs exit main ENDP	

Question 5 (4 marks)

Write a complete assembly language program that:

- Declares uninitialized variables: Signed word integers J, K and L
- Declares uninitialized variables: Unsigned word integers U1, U3 and U3
- Assign the following variables with values: J=3; K=-2; U1=254; U2=22 (In code segment)
- Compute L = J+K and U3 = U1+U2

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