

Philadelphia University Faculty of Engineering

Marking Scheme

Exam Paper

BSc CE

Logic Circuits (630211)

Third Quiz

First semester

Date 16/01/2020

Section 1

Weighting 6% of the module total

Lecturer: Coordinator: Internal Examiner: Dr. Qadri Hamarsheh Dr. Qadri Hamarsheh Eng. Anis Nazer

Marking Scheme Logic Circuits (630211)

Marking Assignments

Question 1		Solution		(6 marks)
FLIP-FLOP NAME	FLIP-FLOP SYMBOL	CHARACTERISTIC TABLE	CHARACTERISTIC EQUATION	EXCITATION TABLE
ŝR	SQ SCik RQ'	S R Q(sext) 0 0 Q 0 1 0 1 0 1 1 1 ?	Q _(sext) = S + R'Q SR = 0	Q Q(sert) S R 0 0 0 X 0 1 1 0 1 0 0 1 1 1 X 0
ЈК	J Q >Cik K Q'	J K Q(sext) 0 0 Q 0 1 0 1 0 1 1 1 Q'	Q(next) = JQ' + K'Q	Q Q(sert) J K 0 0 0 X 0 1 1 X 1 0 X 1 1 1 X 0
D	D Q >Cik Q'	D Q(next) 0 0 1 1	Q(sext) = D	Q Q(sett) D 0 0 0 0 1 1 1 0 0 1 1 1
T	— T Q —>Cik — Q'_	T Q(next) 0 Q 1 Q'	$Q_{(\text{next})} = TQ' + T'Q$	Q Q(sext) T 0 0 0 0 1 1 1 0 1 1 1 0