



Philadelphia University
Faculty of Engineering

Marking Scheme

Quiz Paper

BSc CE

Logic Circuits (630211)

First Quiz

Second semester

Date: 07/03/2020

Section 1

Weighting 5% of the module total

Lecturer:

Dr. Qadri Hamarsheh

Coordinator:

Dr. Qadri Hamarsheh

Internal Examiner:

Eng. Anis Nazer

