

Philadelphia University



Student Name:

Faculty of Engineering

Student Number:

Dept. of Computer Engineering

Second Quiz, First Semester: 2019/2020

Course Title:	Logic Circuits	Date:	05/12/2019
Course No:	630211	Time Allowed:	10 minutes
Lecturer:	Dr. Qadri Hamarsheh	No. Of Pages:	1

Information for candidates

1. This Quiz paper contains 1 question totaling 6 marks
2. The marks for parts of question are shown in round brackets.

Advices to candidates

1. You should attempt all sub questions.
2. You should write your answers clearly.

Question 1

(6 marks)

Design a **Gray Code to BCD converter** by the following procedures:

- a) Write down the **truth table** of the converter. (2 marks)
- b) Apply Karnaugh Map to look for the **minimized logic expressions**. (4 marks)

Solution

GOOD LUCK