Microprocessors (0630371)

Assignment 1:

Lecturer: Dr. Qadri Hamarsheh
Room: 6712
Tel: 96264799000/2492
email: qhamarsheh@philadelphia.edu.jo

Assignment 1:

Weighting: 5% of the Module total
Date set: 10/03/2011
Date Due in: 10/6/2011

Submission Notes:
Completed work must be handed into my office. Sessions for oral discussion will be held in my office. After the deadline a nil mark will be awarded. The only exceptions are where you have permission to hand in later or have been ill and can produce appropriate evidence. In both cases a form (available from the dean secretary must be completed and submitted to the module teacher.

Objectives:
To test the student understanding the basic concepts of the Microprocessor Design and how to build the architecture of the Microprocessor.

Task:

Design and Implement in hardware level hypothetical ALU having 24 operations, the size input data is 8 bits.

Assessment Criteria:

70%: Completing the solution.
30%: Demonstration.

The cover sheet of the handed work must contain the following items:

The Date due in:
The name of the module:
The name of the module teacher:
Your name and number:
Your section: