


Philadelphia University Faculty of Engineering Department of Computer Engineering		Date:- 18/11/2013 Allowed time:- 60 minutes
Microprocessors First Exam		
Student Name: -		ID: -

Q1). Choose the correct answer for the following questions.

1- The linear address of the segment address **6A1E:1F2** is:

- a). 6B322 b). 6A1D3 c). 6A3D2 d). 6B3D2

2- the hexadecimal expression of $1100\ 0001\ 1111\ 1001_2$ is:

- a). C1F9 b). C1E9 c). D2F9 d). F1FA

3- which of the following is a 32 bit data type

- a). DWORD b). QWORD c). TBYTE d). SBYTE

4- which register contains the address of next instruction

- a). EBP b). ESP c). ESI d). EIP

5- the file which contains the starting address, the end address, and size of segment is:

- a). Listing File b). Map File c). Source File d). Object File

Q2). State the differences between RISC and CISC architecture.

Q3). State the differences between Real Addressing mode and protected addressing mode.

Q4). What is the function of the followings:

1- Address bus

2- Data bus

3- Page table.

4- Listing file.

Q5). Write an assembly program that perform the following operations

$Y=X+Z$

$X=Y+25$

$Z=Y-X$

Assume the data above are double word and initialize the parameter by data of your choice.